

# Integrated Circuit (IC)

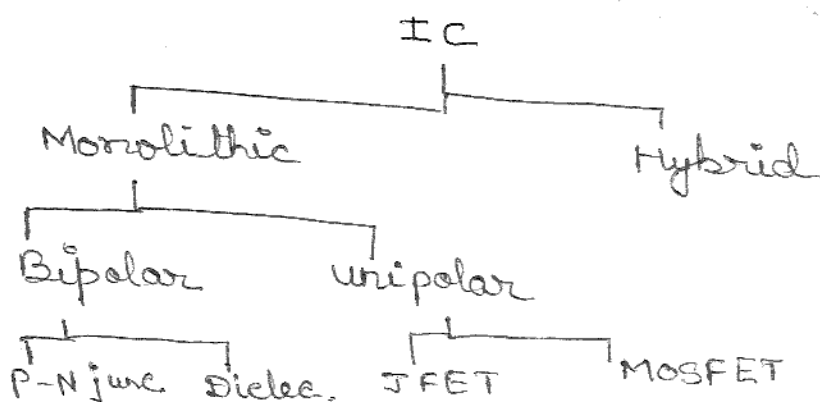
→ Miniature form of electronic circuit.

→ Active and passive component in single crystal (Si).

## → Advantage

- low cost, miniature form.
- less power.
- Reliability and performance.
- Mobility
- Speed
- Increased package density.

★ Linear or Digital IC.



→ Amplifier

It increases the gain of the input signal

$$\text{gain} = \frac{\text{O/P}}{\text{I/P}}$$

In case of operational amplifier

$$V_o \propto (V_1 - V_2)$$

$$V_o = A_d (V_1 - V_2)$$

$$V_o = A_d V_d$$

$$\frac{V_1 + V_2}{2} = \text{common mode signal}$$

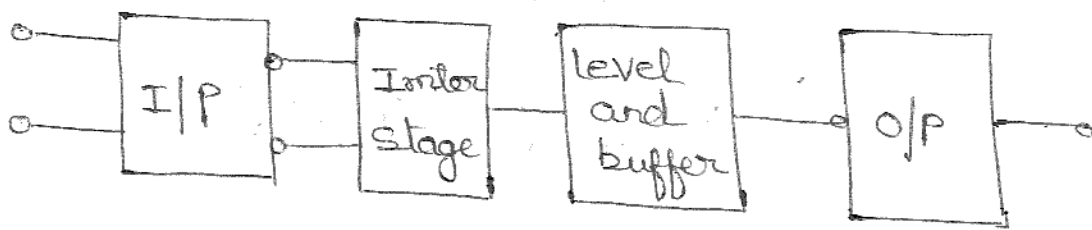
$$V_c = \frac{V_1 + V_2}{2}$$

$$V_o \propto V_c$$

$$V_o = A_c V_c$$

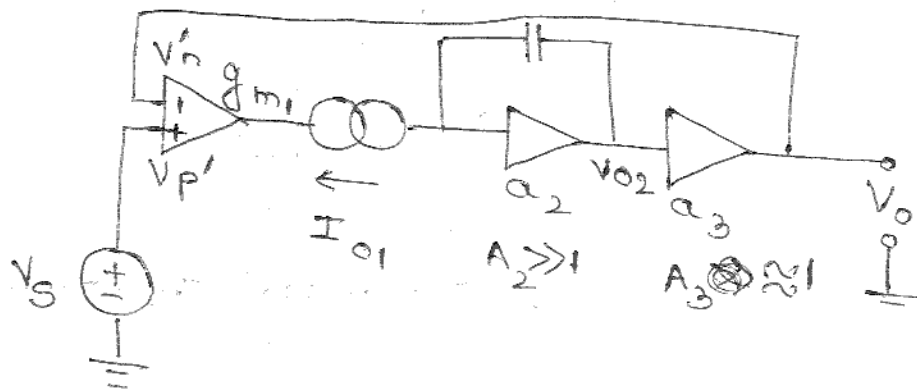
$$V_o = A_d V_d + A_c V_c$$

• Block Diagram



Buffer have unity voltage gain

\* Method Of Improving Slew Rate



$$S = \frac{I_{max}}{C}$$

$$S = \frac{I_{o1} \text{ sat}}{C}$$

$$I_{o1} \text{ sat} = C \frac{dV_{o2}}{dt}$$

$$a_3 \approx 1$$

$$V_o \approx V_{o2}$$

$$\frac{dV_o}{dt} \text{ max} = \frac{dV_{o2}}{dt} = \frac{I_{o1} \text{ sat}}{C}$$

$$I_{o1} = g_{m1} (V_p' - V_n')$$

$$V_{o2} = Z g_{m1} (V_p' - V_n')$$

$$= \frac{1}{j\omega C} g_{m1} (V_p' - V_n')$$

$$\frac{V_{o2}}{V_p' - V_n'} = \frac{g_{m1}}{j\omega C} \quad |a| = \frac{g_{m1}}{2\pi f C}$$

$$|a| f = \frac{g_{m1}}{2\pi C}$$

$$f_t = \frac{g_{m1}}{2\pi C}$$

↓

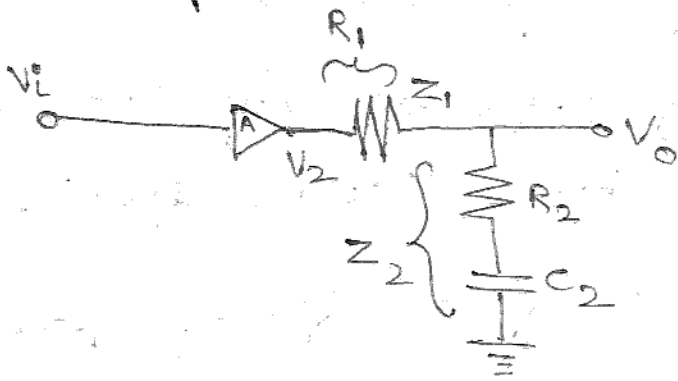
gain bandwidth product

$$C = \frac{g_{m1}}{2\pi f_t}$$

$$S = \frac{I_{o1} \text{sat}}{C}$$

$$S = \frac{I_{o1} \text{sat} \cdot 2\pi f_t}{g_m}$$

## \* Frequency Compensation Pole Zero Method



Uncompensated transfer function  $A$ , zero should be higher freq. than pole.

Transfer of compensation network

$$\frac{V_o}{V_2} = \frac{Z_2}{Z_1 + Z_2}$$

$$Z_1 = R_1 \quad Z_2 = R_2 + \frac{1}{j\omega C_2}$$

$$\frac{V_o}{V_2} = \frac{R_1}{R_1 + R_2 + \frac{1}{j\omega C_2}} = \frac{1 + jf/f_1}{1 + jf/f_2}$$

where,

$$f_1 = \frac{1}{2\pi R_2 C_2} \quad f_2 = \frac{1}{2\pi (R_1 + R_2) C_2}$$

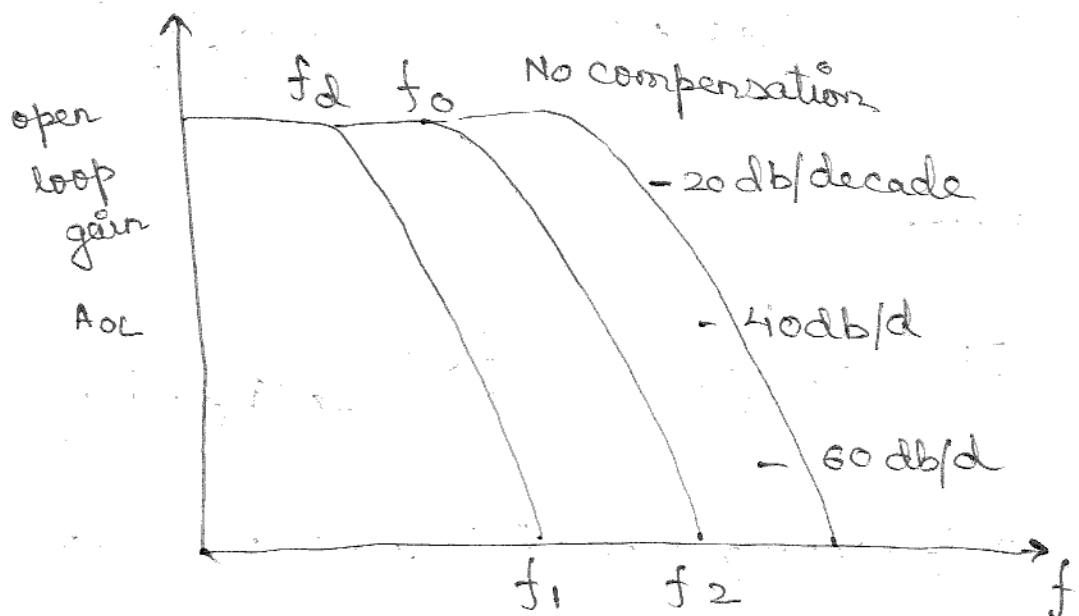
Compensating  $n/p$  will have zero at corner freq. of  $f_1$ .

$f_1$  will cancel the pole effect. The pole of compe. s/w will be at corner freq.  $f_0$  we shift the compensating s/w thro  $f_2$  (corner freq). This freq. can be found from graph.

$$A' = \frac{V_o}{V_i} = \frac{V_o}{V_2} \times \frac{V_2}{V_i}$$

$$= \frac{AR_2}{R_1 + R_2} \frac{1 + jf/f_1}{1 + jf/f_0}$$

$$= \frac{AOL}{(1 + jf/f_1)(1 + jf/f_2)(1 + jf/f_3)}$$

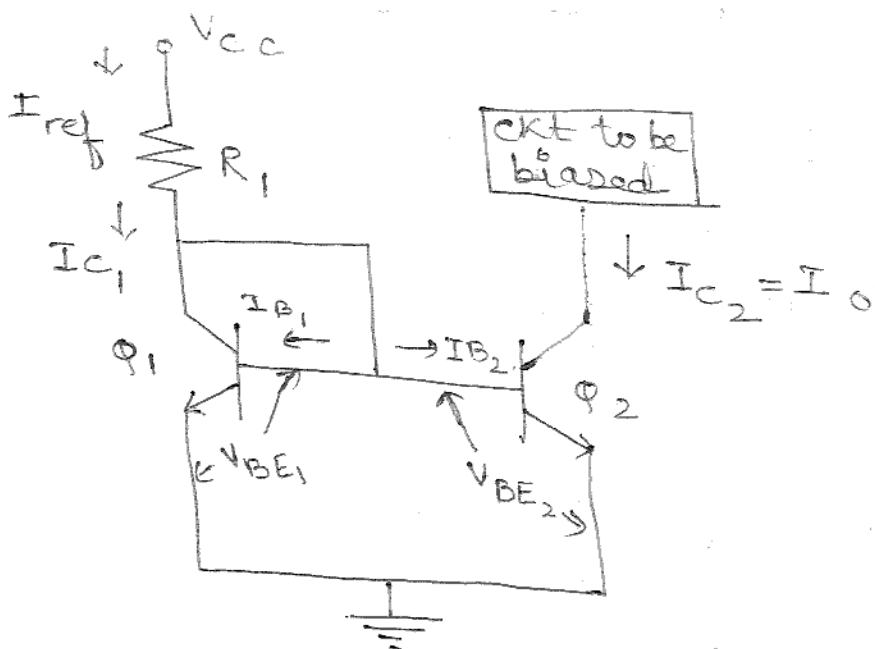


### \* Internal Compensation

- I) Slow charging signal
- II) not required good freq response
- III) Internal capacitance 30pf shunt
- IV) Reduces op signal at higher freq.

### \* Current Source (Current mirror)

- I) Transistors in active region.
- II)  $I_c$  is independent of collector voltage.
- III)  $\phi_1$  and  $\phi_2$  matched.



## Analysis

$$\frac{I_{C1}}{I_E} \approx \alpha_F e^{V_{BE1}/V_T}$$

$$\frac{I_{C2}}{I_E} \approx \alpha_F e^{V_{BE2}/V_T}$$

$$\frac{I_{C2}}{I_{C1}} = e^{(V_{BE2} - V_{BE1})/V_T}$$

$$V_{BE2} = V_{BE1}$$

$$I_{C2} = I_{C1} = I_C = I_O$$

$$\beta_1 = \beta_2 = \beta$$

Kcl at  $\phi_1$  gives

$$I_{ref} = I_{C1} + I_{\beta_1} + I_{\beta_2}$$

$$= I_{C1} + \frac{I_C}{\beta_1} + \frac{I_{C2}}{\beta_2}$$

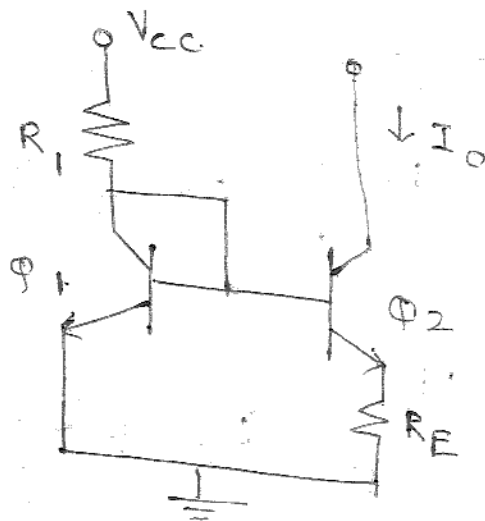
$$= I_C \left[ 1 + \frac{1}{\beta} + \frac{1}{\beta} \right]$$



$$I_{ref} = I_c \left[ \frac{\beta + 2}{\beta} \right]$$

$$= I_c \left[ 1 + \frac{2}{\beta} \right]$$

★ Widlar Current Source



$$I_{c2} \ll I_{c1}$$

$$\frac{I_{c1}}{I_{c2}} = e^{(V_{BE1} - V_{BE2}) / V_T}$$

$$V_{BE1} - V_{BE2} = V_T \ln \left( \frac{I_{c1}}{I_{c2}} \right) \quad \text{--- (1)}$$

~~$$V_{BE1} = V_{BE2}$$~~

KVL at base emitter loop

$$V_{BE1} = V_{BE2} + (I_{B2} + I_{c2}) R_E$$

$$V_{BE1} - V_{BE2} = \left( \frac{I_{c2}}{\beta} + I_{c2} \right) R_E$$

Equating ① and ②

$$V_T \ln \left( \frac{I_{C1}}{I_{C2}} \right) = I_{C2} \left( \frac{1}{\beta} + 1 \right) R_E$$

$$R_E = \frac{V_T \ln \left( \frac{I_{C1}}{I_{C2}} \right)}{\left( \frac{1}{\beta} + 1 \right) I_{C2}}$$

$$I_{ref} = I_{B1} + I_{B2} + I_{C1}$$

$$= \left( 1 + \frac{1}{\beta} \right) I_{C1} + \frac{I_{C2}}{\beta}$$

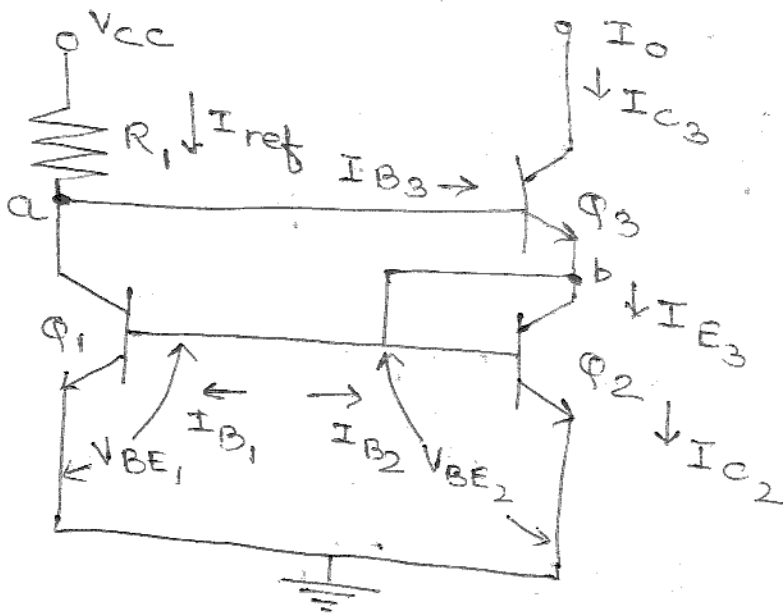
$$= \left( \frac{\beta + 1}{\beta} \right) I_{C1} + \frac{I_{C2}}{\beta}$$

$I_{C2}$  is very small

$$I_{ref} = \left( \frac{\beta + 1}{\beta} \right) I_{C1}$$

$$I_{ref} \approx I_{C1}$$

# ★ Wilson Current Source



Analysis

$$V_{BE1} = V_{BE2}$$

$$I_{C1} = I_{C2}$$

$$I_{B1} = I_{B2} = I_B$$

at node b

$$I_{E3} = I_{B1} + I_{B2} + I_{C2}$$

$$= I_{C2} + 2I_B = I_{C2} + \frac{2I_{C2}}{\beta}$$

$$I_{E3} = I_{C2} \left( \frac{2}{\beta} + 1 \right) \quad \text{--- (1)}$$

$$I_{E3} = I_{C3} + I_{B3}$$

$$= I_{C3} \left( 1 + \frac{1}{\beta} \right) \quad \text{--- (2)}$$

Eq. ① and ②

$$I_{C2} \left( \frac{2}{\beta} + 1 \right) = I_{C3} \left( \frac{\beta + 1}{\beta} \right)$$

$$I_{C3} = I_{C2} \left( \frac{2 + \beta}{\beta + 1} \right)$$

$$I_{C3} = I_0 = I_{C2} \left( \frac{2 + \beta}{\beta + 1} \right)$$

$$I_{C3} = I_0 = I_{C1} \left( \frac{\beta + 2}{\beta + 1} \right)$$

$$I_{\text{ref}} = I_{B3} + I_{C1}$$

$$= \left( \frac{\beta + 1}{\beta + 2} \right) I_0 + \frac{I_0}{\beta}$$

$$I_{\text{ref}} = I_0 \left[ \frac{\beta + 1}{\beta + 2} + \frac{1}{\beta} \right]$$